

**FENWICK & WEST LLP**

SAN FRANCISCO OFFICE | EMBARCADERO CENTER WEST

275 BATTERY ST., SUITE 1500 | SAN FRANCISCO, CA 94111

TEL 415.875.2300 | FAX 415.281.1350 | WWW.FENWICK.COM

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CENTRAL FAX CENTER**FACSIMILE TRANSMISSION****JAN 20 2005****CONFIDENTIAL**DATE: January 20, 2005

CLIENT-MATTER No.: 22120-06499

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NAME	FAX No.	PHONE No.
Examiner Thuan V. Do Group Art Unit 2825 Commissioner for Patents	(703) 872-9306	

FROM: Rimma Budnitskaya
Reg. No. 48,237

PHONE: (415) 875-2401

SENT BY: Tiffany Bell

PHONE: (415) 875-2445

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MESSAGE:

Application No.: 10/029,521

Filing Date: December 21, 2001

Inventor(s): Christopher F. Bevis

*Please See Attached Comments on Statement of Reasons for Allowance***CAUTION - CONFIDENTIAL**

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22120/06499/SF/5136033.1

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICERECEIVED
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JAN 20 2005

APPLICANT: Christopher F. Bevis
SERIAL NO.: 10/029,521
FILING DATE: December 21, 2001
TITLE: Inspection of Semiconductor Devices Using Design Data
EXAMINER: Thuan V. Do
GROUP ART UNIT: 2825
ATTY. DKT. NO.: 22120-06499

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Comments on Statement of Reasons for Allowance

In response to the Reasons for Allowance provided by Examiner in the Notice of Allowance mailed on December 14, 2004, Applicant respectfully disagrees with the Examiner's reasons. Applicant submits the following reasons for allowance: the claimed invention is patentable over Nara because Nara does not disclose or suggest at least "the configuration of the recipe is performed before a wafer printed with a mask created from the mask set data is

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generated” and “the recipe is created before a wafer to which the design data has been applied is generated.”

Applicant respectfully requests that the Examiner reconsider reasons for allowance in light of the comments provided by the Applicant.

RESPECTFULLY SUBMITTED,
Christopher F. Bevis

Date: January 20, 2005

By: 

Rimma Budnitskaya, Reg. No. 48,237
FENWICK & WEST LLP
Silicon Valley Center
801 California Street
Mountain View, CA 94041
Phone: (415) 875-2401
Fax: (415) 281-1350